

# SAI DINESH KONAKALLA

E-mail: [saidineshk012@gmail.com](mailto:saidineshk012@gmail.com)

Contact#+919494720409

Sri Vasavi Engineering College

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## **CAREER OBJECTIVE:**

I flourish my skills and talents to promote the organization.

## **SCHOLASTIC PROFILE:**

- Electronics and Communications engineer pursuing at Sri Vasavi Engineering College with 6.66(GPA),Pedatadepalli(2016-2020).
- Secured Board Of Intermediate education certificate from Pragati Junior College with 81.5%,Tanuku(2016).
- Achieved first division in Secondary School Certification from Manasa English Medium High School with 7.2(GPA), veerabhadrapuram (2015).

## **Project Details:**

- **Project title:** High Speed Real Valued Fast Fourier Transform
- **Project Technology Used:** Xilinx.
- **Description:** To increase the Computational speed Of FastFourier Transform by Using The advantage of Real Values.

## **KEYSKILLS:**

- PYTHON
- Image Processing
- Hands-on experience in VLSI Lab
- Hands-on experience in Micro Processor Lab
- Effective communication

## **INTERPERSONAL SKILLS:**

- Loyalty
- Dedication
- Hardworking nature

## **ACHIEVEMENTS:**

- Secured First position in “KABADDI” from my school

## **CO-CURRICULAR ACTIVITIES:**

- Presented a paper on “SENSORS” at Sri Vasavi Engineering College, Pedatadepalli.
- Participated in the Workshop on “Sci lab” and “Aurdino Interfacing” held at Sri Vasavi Engineering College.
- Participated in “Augmented reality” Workshop held at SRKR.
- Participated in “IOT” at JNTUK

## **PERSONAL PROFILE:**

Name: Sai dinesh konakalla

Date of Birth: 28-11-1998

Father's Name: Konakalla satyanarayana

Languages known: English, Telugu

Hobbies: Dancing, playing shuttle, watching movies

Address: Bossu bumma center,, Raja pangidigudem,, D/NO 3-37, Dwarakatirumal mandalam, west Godavari distric pin code: 534425

Place:

Date:

K.Sai dinesh